

## Electronic Patent Application Fee Transmittal

<b>Application Number:</b>	10751324			
<b>Filing Date:</b>	05-Jan-2004			
<b>Title of Invention:</b>	INTEGRATED CIRCUITS WITH RAM AND ROM FABRICATION OPTIONS			
<b>First Named Inventor/Applicant Name:</b>	Raminda Udaya Madurawe			
<b>Filer:</b>	Jose S. Garcia/Donna Petford			
<b>Attorney Docket Number:</b>				
Filed as Large Entity				
<b>Utility under 35 USC 111(a) Filing Fees</b>				
Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
<b>Basic Filing:</b>				
Maintenance Fee Due at 3.5 years	1551	1	980	980
Surcharge after Expiration-Unintentional	1558	1	1640	1640
<b>Pages:</b>				
<b>Claims:</b>				
<b>Miscellaneous-Filing:</b>				
<b>Petition:</b>				
<b>Patent-Appeals-and-Interference:</b>				
<b>Post-Allowance-and-Post-Issuance:</b>				

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
<b>Extension-of-Time:</b>				
<b>Miscellaneous:</b>				
<b>Total in USD (\$)</b>				<b>2620</b>